



Steven R. Seidel
1951-2014



Professional Background

- Ph.D 1979 – University of Iowa
- Professor of Computer Science, Michigan Tech
- PGAS Implementations, Algorithms and Optimizations. Early Hypercube algorithms, Exploration of early multiprocessor architectures, UPC, Parallel Simulation, Communication and Networking strategies for HPC
- Extensive Service in Community



PGAS Contributions

- 2009 PGAS Conference Program Chair
- Original Collective Specification for Unified Parallel C and working on 1-sided collectives
- Collaborated on UPC Memory Model
- Active in Initial UPC Forum leading to early UPC Specifications
- Lead Michigan Tech UPC Compiler Project -MuPC
- Reference implementation of AMOs and Collectives for UPC
- Parallel algorithms and applications using UPC





Personal Reflections

“Steve was a good friend and a good guy: honest, devoted to his work and family” David Poplawski, MTU.

“...he was especially helpful to me when I was a young assistant professor...Steve had a way of mentoring that didn’t feel like mentoring; it just felt like working together as equals. This was a tremendous boost for my self-confidence during those early year...”. “Steve acted as a great sounding board and sanity checker”. Chuck Wallace, MTU.

“He was demanding of students and had high expectations, but he was fair,... He wanted them to have the right foundation, one that would stick with them for a long time.” Linda Ott, Chair, MTU Computer Science Dept.



Building Hardware





High Performance Cars!





Synchronizing Trains

